

DECLARATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or the below named inventors are the original, first and joint inventors (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled "**BUS BRIDGE CIRCUIT INCLUDING AUDIO LOGIC AND AN ADDRESSABLE REGISTER FOR STORING AN ADDRESS BIT USED WHEN THE AUDIO LOGIC ACCESSES DIGITAL DATA, AND METHOD FOR INITIALIZING A CHIP SET INCLUDING THE BUS BRIDGE CIRCUIT**" the Specification of which:

is attached hereto.  
 was filed on \_\_\_\_\_ as Application Serial No. \_\_\_\_\_.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose to the Patent and Trademark Office all information known to me to be material to patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent, United States provisional application(s), or inventor's certificate listed below and have also identified below any foreign application for patent, United States provisional application, or inventor's certificate having a filing date before that of the application on which priority is claimed:

PRIORITY APPLICATION(S)			Priority Claimed
(Number)	(Country)	(Date Filed)	Yes/No
(Number)	(Country)	(Date Filed)	Yes/No

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose all information known to me to be material to patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations, § 1.56, which become available between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)	(Status)
(Application Serial No.)	(Filing Date)	(Status)

I hereby direct that all correspondence and telephone calls be addressed to Thomas H. Belvin, Patent Agent, Williams, Morgan & Amerson, P.C., 7676 Hillmont, Suite 250, Houston, Texas 77040, (512) 467-7755.

I HEREBY DECLARE THAT ALL STATEMENTS MADE OF MY OWN KNOWLEDGE ARE TRUE AND THAT ALL STATEMENTS MADE ON INFORMATION AND BELIEF ARE BELIEVED TO BE TRUE; AND FURTHER THAT THESE STATEMENTS WERE MADE WITH THE KNOWLEDGE THAT WILLFUL FALSE STATEMENTS AND THE LIKE SO MADE ARE PUNISHABLE BY FINE OR IMPRISONMENT, OR BOTH, UNDER SECTION 1001 OF TITLE 18 OF THE UNITED STATES CODE AND THAT SUCH WILLFUL FALSE STATEMENTS MAY JEOPARDIZE THE VALIDITY OF THE APPLICATION OR ANY PATENT ISSUED THEREON.

Inventor's Full Name: RICHARD A. ZATORSKI  
Inventor's Signature: *Richard A. Zatorski*  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

**RICHARD A. ZATORSKI**

Serial No.: **Unknown**

Filed: **Concurrently Herewith**

For: **BUS BRIDGE CIRCUIT INCLUDING AUDIO LOGIC  
AND AN ADDRESSABLE REGISTER FOR STORING  
AN ADDRESS BIT USED WHEN THE AUDIO LOGIC  
ACCESES DIGITAL DATA, AND METHOD FOR  
INITIALIZING A CHIP SET INCLUDING THE BUS  
BRIDGE CIRCUIT**

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§ Examiner: Unknown  
§  
§  
§ Group Art Unit: Unknown  
§  
§ Atty. Docket: 2070.001700/P5483

**POWER OF ATTORNEY**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

The undersigned, being the inventors named in the above-identified application, hereby revoke any previous Powers of Attorney and appoint:

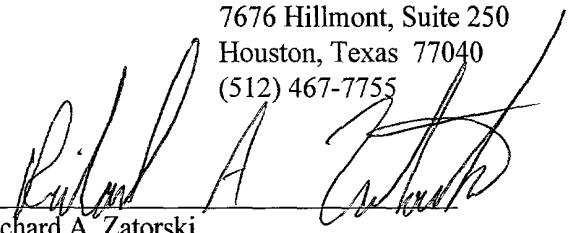
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Date: 6/28/01